B.E. MBA integrated in Computer Science & Engineering

FOR 2011-12
### Scheme of Examination of B.E. MBA integrated in Computer Science & Engineering

#### Second Year - Third Semester B.E. MBA (CSE)

<table>
<thead>
<tr>
<th>Ref. No</th>
<th>Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Total</th>
<th>Credits</th>
<th>University External Marks</th>
<th>Internal Sessional Marks</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSE311</td>
<td>Data Structures</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>CSE361</td>
<td>Data Structures (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>2</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td>CSE312</td>
<td>Computer Architecture &amp; Organization</td>
<td>4</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>CSE313</td>
<td>Peripheral Devices &amp; Interfaces</td>
<td>4</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>CSE363</td>
<td>Hardware Lab (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>2</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td>IBM301</td>
<td>Organization Behavior</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>EC316</td>
<td>Digital Electronics</td>
<td>4</td>
<td>0</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>EC366</td>
<td>Digital Electronics (Practical)</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>2</td>
<td>1</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td>18</td>
<td>02</td>
<td>08</td>
<td>28</td>
<td>25</td>
<td>250</td>
<td>400</td>
</tr>
</tbody>
</table>

#### Second Year - Fourth Semester B.E. MBA (CSE)

<table>
<thead>
<tr>
<th>Ref. No</th>
<th>Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Total</th>
<th>Credits</th>
<th>University External Marks</th>
<th>Internal Sessional Marks</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSE411</td>
<td>Analysis &amp; Design of Algorithms</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>CSE461</td>
<td>Analysis &amp; Design of Algorithms (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>2</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td>CSE412</td>
<td>Database Management System</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>CSE462</td>
<td>Database Management System (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>2</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td>CSE414</td>
<td>Object Oriented Programming</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>CSE464</td>
<td>Object Oriented Programming (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>2</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td>IBM401</td>
<td>Management of Information Technology</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>EC416</td>
<td>Microprocessors</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>4</td>
<td>50</td>
<td>50</td>
</tr>
<tr>
<td>EC466</td>
<td>Microprocessors (Practical)</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>2</td>
<td>2</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td>15</td>
<td>05</td>
<td>11</td>
<td>31</td>
<td>28</td>
<td>250</td>
<td>450</td>
</tr>
</tbody>
</table>
## Third Year - Fifth Semester B.E. MBA (CSE)

<table>
<thead>
<tr>
<th>Ref. No</th>
<th>Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Total</th>
<th>Sess.</th>
<th>Paper / Viva</th>
<th>Total</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSE501</td>
<td>Operating System</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE551</td>
<td>Operating System (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>CSE502</td>
<td>Software Engineering</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE552</td>
<td>Software Engineering (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>CSE503</td>
<td>Computer Network</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE553</td>
<td>Computer Network (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>CSE504</td>
<td>Principle of Programming Languages</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>IBM 501</td>
<td>Marketing Management</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>IBM 502</td>
<td>Human Resource Management</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE506</td>
<td>Industrial Training (After 4&lt;sup&gt;th&lt;/sup&gt; Sem)</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>50</td>
<td>--</td>
<td>50</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td>18</td>
<td>4</td>
<td>9</td>
<td>31</td>
<td>575</td>
<td>825</td>
<td>1400</td>
</tr>
</tbody>
</table>

## Third Year - Sixth Semester B.E. MBA (CSE)

<table>
<thead>
<tr>
<th>Ref. No</th>
<th>Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Total</th>
<th>Sess.</th>
<th>Paper / Viva</th>
<th>Total</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSE601</td>
<td>Web Technologies</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE651</td>
<td>Web Technologies (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>IBM 601</td>
<td>Managerial Economics</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE603</td>
<td>Computer Graphics</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE653</td>
<td>Computer Graphics (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>CSE604</td>
<td>Artificial Intelligence</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE654</td>
<td>Artificial Intelligence (Practical)</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>2</td>
<td>50</td>
<td>50</td>
<td>100</td>
</tr>
<tr>
<td>CSE605</td>
<td>Modeling &amp; Simulation</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE655</td>
<td>Modeling &amp; Simulation (Practical)</td>
<td>0</td>
<td>0</td>
<td>2</td>
<td>2</td>
<td>50</td>
<td>50</td>
<td>100</td>
</tr>
<tr>
<td>IBM 602</td>
<td>Corporate Legal Environment</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td>18</td>
<td>4</td>
<td>10</td>
<td>32</td>
<td>550</td>
<td>850</td>
<td>1400</td>
</tr>
</tbody>
</table>
**Fourth Year - Seventh Semester B.E. MBA (CSE)**

<table>
<thead>
<tr>
<th>Ref. No</th>
<th>Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Total</th>
<th>Sess.</th>
<th>Paper / Viva</th>
<th>Total</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSE701</td>
<td>Compiler Design</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE751</td>
<td>Compiler Design (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>CSE702</td>
<td>Multimedia System Design</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE703</td>
<td>Software Testing &amp; Quality Assurance</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE753</td>
<td>Software Testing &amp; Quality Assurance (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>IBM-701</td>
<td>Entrepreneurship and Project Management</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE757</td>
<td>Project – I</td>
<td>0</td>
<td>0</td>
<td>8</td>
<td>8</td>
<td>200</td>
<td>100</td>
<td>300</td>
</tr>
<tr>
<td>IBM-702</td>
<td>Industrial Relations</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE709</td>
<td>Industrial Training (After 6th Semester)</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>-</td>
<td>50</td>
<td>-</td>
<td>50</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td>12</td>
<td>3</td>
<td>16</td>
<td>31</td>
<td>650</td>
<td>750</td>
<td>1400</td>
</tr>
</tbody>
</table>

**Fourth Year - Eighth Semester B.E. MBA (CSE)**

<table>
<thead>
<tr>
<th>Ref. No</th>
<th>Subject</th>
<th>L</th>
<th>T</th>
<th>P</th>
<th>Total</th>
<th>Sess.</th>
<th>Paper / Viva</th>
<th>Total</th>
</tr>
</thead>
<tbody>
<tr>
<td>CSE801</td>
<td>Advanced Database Systems</td>
<td>3</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE802</td>
<td>Digital Image Processing &amp; Computer Vision</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE852</td>
<td>Digital Image Processing &amp; Computer Vision (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>IBM-801</td>
<td>Research Methodology</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE855</td>
<td>Java Technologies (Practical)</td>
<td>0</td>
<td>0</td>
<td>3</td>
<td>3</td>
<td>75</td>
<td>75</td>
<td>150</td>
</tr>
<tr>
<td>IBM-802</td>
<td>Financial Management</td>
<td>3</td>
<td>1</td>
<td>0</td>
<td>4</td>
<td>50</td>
<td>100</td>
<td>150</td>
</tr>
<tr>
<td>CSE858</td>
<td>Project – II</td>
<td>0</td>
<td>0</td>
<td>8</td>
<td>8</td>
<td>200</td>
<td>100</td>
<td>300</td>
</tr>
<tr>
<td><strong>Total</strong></td>
<td></td>
<td>12</td>
<td>3</td>
<td>14</td>
<td>29</td>
<td>550</td>
<td>650</td>
<td>1200</td>
</tr>
</tbody>
</table>
SYLLABUS FOR
B.E. MBA integrated in Computer Science & Engineering (2010-11)

THIRD SEMESTER

Paper Title: DATA STRUCTURES

Paper Code: CSE311  Max. Marks (Final Exam): 50  Time: 3 Hours
Credits:04  Max. Marks (Sessional Exam): 50

Total Lectures: 45  L T P 3 1 0

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, selecting at least two from each section.

Objectives: This course should provide the students with a fairly good concept of the fundamentals of different types of data structures and also the ways to implement them. Algorithm for solving problems like sorting, searching, insertion & deletion of data etc. related to data structures should also be discussed. After completion of this subject student should be able to choose an appropriate data structure for a particular problem.

SECTION – A

Linear Data Structures:
Sequential representations – Arrays (one, two, multi dimensional) and Records, Binary Search, Stacks, Queues and Circular queues; Link Representation - Linear linked lists, circularly linked lists. Doubly linked lists, Garbage collection and Compaction. (12)

Recursion:
Design of recursive algorithms, Tail Recursion, When not to use recursion, Removal of recursion. (03)

Sorting Algorithms:
Bubble sort, Selection Sort, Insertion Sort, Quick Sort, Merge Sort, Heap sort and Radix Sort. (07)

SECTION – B

Non-linear Data Structure:

Hashing:
Hashing Functions, collision Resolution Techniques. (02)

File Structures:
Index Techniques: Hashed Indexing, Tree Indexing – B Trees; File Organizations: Sequential, Random, Linked Organizations, Inverted Files. (06)

Text Books:

References:
   : Art of Computer Programming, Volume 3:
Paper Title: DATA STRUCTURES (Practical)

**Paper Code:** CSE361  
**Credits:** 02  
**Max. Marks:** 50  
**L T P:** 0 0 3  

**Note:** At least ten practical should be covered based on the following directions:

- Implementation of array operations: Traversal, Insertion & Deletion at and from a given location
- Stacks: Implementation of Push, Pop; Conversion of Infix expression to Postfix, Evaluation of Postfix expressions.
- Queues: Circular Queue: Adding & deleting elements.
- Linked list: inserting, deleting, implementation of stacks & queues using linked lists; Polynomial addition.
- Implementation of Graphs
- Implementation of sorting and searching algorithms
- Hash tables implementation: searching, inserting and deleting

Paper Title: COMPUTER ARCHITECTURE AND ORGANIZATION

**Paper Code:** CSE312  
**Credits:** 04  
**Max. Marks (Final Exam):** 50  
**Max. Marks (Sessional Exam):** 50  
**Time:** 3 Hours  
**Total Lectures:** 45  
**L T P:** 4 0 0  

**Note:** Examiner will set eight questions covering four questions from each section. Candidates will be required to attempt five questions, selecting at least two from each section.

**Objectives:** This course offers a good understanding of the various functional units of a computer system and prepares the student to be in a position to design a basic computer system.

**SECTION – A**

**Register Transfer Language and Micro-Operations:**  
Basic Concepts, Complements, Fixed and Floating Point Representation, Register Transfer Language, Inter Register Transfer Arithmetic, Bus and Memory Transfers, Arithmetic, Logic and Shift Micro-Operations, Arithmetic Logic Shift Unit.

**Basic Computer Origination and Design:**  
Instruction Codes, Computer Instructions, Timing and Control, Execution of Instructions, Input Output and Interrupt, Design of Basic Computer.

**Programming the Basic Computer:**  

**Central Processing Unit:**  
General Register Organization, Stack Organization, Instruction Formats, Addressing Modes, Data Transfer and Manipulation, Program Control.

**SECTION – B**
**Microprogrammed Control and Pipelining:**
Control Memory, Address Sequencing, Microinstruction Formats, Pipelining, Arithmetic and Instruction Pipelining.

**Computer Arithmetic:**
Addition and Subtraction of unsigned Binary Numbers, Addition, Subtraction, Multiplication and Division Algorithm.

**Input-Output Organization:**
Input-Output Interface, Asynchronous Data Transfer, DMA, Priority Interrupt, I/O Processor, Serial Communication.

**Memory Organization:**
Memory Hierarchy, Associative Memory, Virtual Memory, Cache Memory, Memory Management Hardware.

**Text Book:**

**References:**

---

**Paper Title: PERIPHERAL DEVICES & INTERFACES**

**Paper Code:** CSE313

**Credits:** 04

**Max. Marks (Final Exam):** 50

**Max. Marks (Sessional Exam):** 50

**Time:** 3 Hours

**Total Lectures:** 45

L T P   4  0  0

**Note:** Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions by selecting at least two from each section.

**Objectives:** The objective of this course is to provide knowledge about integrated circuit memories & the functional details of various peripheral devices.

---

**SECTION – A**

**The Memory Element:**
RAM, Linear Select Memory Organization, Decoders, Dimensions of Memory access, connecting Memory chips to a computer bus, Static RAM, Dynamic RAM, ROM, Digital recording techniques.

**System Resources:**
Interrupt, DMA Channel, I/O Port Addresses and resolving and resolving the conflict of resources. I/O buses- ISA, PCI, SCSI, EISA, Local bus, VESA Local bus, PCI bus, PCI Express, Accelerated graphics port bus.

---

**SECTION – B**

**Video Hardware :**
Introduction to Multimedia Kit, Multimedia building blocks, Video display technologies, DVI Digital signals for CRT Monitor, LCD Panels, Video adapter types, Integrated Video/ Motherboard chipset, Video RAM, Video driver and multiple Monitor, Graphic accelerators, Advanced 3D Techniques.

**Input/ Output Driver software aspects:**
Role of device driver, DOS and UNIX/ LINUX device drivers. Design & Integration of Peripheral devices to a computer system as a Case Study

**Text Book:**
1. T.C. Bartee
2. Scott Muller

References:
1. Douglas V Hall
2. Silbersehatz and Galvin
   : Operating System Concepts, Addison Wesley Inc.
3. P. Pal Chandhari
4. Del Corso, H.Kirrman, JD Nicond

Paper Title: HARDWARE LAB. (PRACTICAL)

Paper Code: CSE363
Credits:02
Maximum Marks: 50
L T P: 0 0 3

Note: Practical based on:
1. Introduction to various hardware elements:
   Monitors, Printers, Keyboards, Mouse, Different memory elements.
2. Different types of buses and their interfaces.
3. Assembly of PC
4. Introduction to Network elements:
   Routers, Bridges, hubs, Switches, hardware tools and management tools.

Paper Title: Organization Behavior (Theory)

Paper Code: IBM 301
Credits:04
Time: 3 Hours
Total Lectures: 45
L T P 3 1 0

Note: Examiner shall set eight questions, four from Part-A and four from Part-B of the syllabus. Candidate will be required to attempt any five questions selecting at least two questions from Part-A and two from Part-B.

Part A

Introduction to Organization Behavior
[10]
Definition and meaning of OB, impact of other sciences (Anthropology, Sociology, Psychology) on OB, perception, self esteem, attitude & personality, meaning of culture, impact of technology on OB.

Motivation, Learning & Leadership
[13]
Meaning of Motivation, Content theories of motivation (Maslow’s Hierarchy of needs, Herzberg’s two factor theory), Process theories (Vroom’s Expectancy theory, Porter-Lawler Model), Motivation applied (Job design, job rotation, goal
setting, MBO), various methods of motivating employees, Behavioral & Cognitive theories of learning, Leadership theories (Trait theory, Fiedler’s Contingency theory, Path–Goal leadership theory), Leadership styles (Blake & Mouton managerial grid, Hersey & Blanchard’s life cycle approach)

**Part B**

**Group behavior:**
Group Dynamics, conflict, power & politics, Group behavior, types of groups, group decision making, conflict in organizations and reason, interpersonal conflict, inter group conflict, meaning of power, classification of power, politics in organizations

**Organization environment & Communication**
Authority & responsibility, delegation and division of work, quality of work life, communication process, modes of communication in organization and barriers to communication, formal & informal communication,

**Recommended Books:**
2. Organization Behavior– Fred Luthans, Tata Mcgraw Hill

**Paper Title: DIGITAL ELECTRONICS**

| Paper Code: EC316 | Max. Marks (Final Exam): 50 | Time: 3 Hours |
| Credits:04 | Max. Marks (Sessional Exam): 50 | Total Lectures: 45 |
| | | L T P: 4 0 0 |

*Note:* Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions by selecting at least two from each section.

**Objectives:**

**SECTION – A**

**Introduction:**

**Number System and Code:**
Decimal Binary, Hexadecimal, Octal’s complement, 2’s complement, addition and substraction, weighted binary codes, Error detecting codes, Error correcting codes, Alphanumeric codes

**Counters & Shift Register:**
Ripple Counters, Design of Modulo-N ripple counter, Up-Down counter, design of synchronous counters with-and without lockout conditions, design of shift registers with shift-left, shift-right &, parallel load .facilities, universal shift registers.
Data Converters:
Sample & Hold switch, D/A converters: Weighted type, R-2R ladder type; A/D Converters: Counter-Ramp' type, Dual Slope Type, Successive approximation type, flash type; Specifications of ADC & DAC.

SECTION – B

Digital Logic Families:
Characteristics of digital circuits: Fan in, fan out, power dissipation, propagation delay, noise margin; Transistor-transistor Logic (TTL), TIL, NAND Gate with active Pull Up, its input and output Characteristics, Types of TTL Gates (Schottky, standard, low power, high speed). Emitter Coupled Logic (ECL), ECL gate, its transfer characteristics, level translation in ECL & TTL, MOS Gates, MOS Inverter, CMOS Inverter, Rise & Fait time of MOS & CMOS gates, Interfacing TIL & CMOS Circuits, Comparison of Characteristics of TTL, ECL, MOS & CMOS logic circuits, Tristate Logic & its applications.

Semiconductor Memories & Programmable Logic:
RQM, PROM, EPROM, EEPROM; RAM: Static RAM, Typical Memory Cell, Memory Organization, Dynamic RAM cell, Reading & Writing Operation in RAM, PLA, PAL & FPGA

Text Books:

Paper Title: DIGITAL ELECTRONICS, (PRACTICAL)

Paper Code: EC366
Credits: 01
Max. Marks: 50
L T P: 0 0 2

Note: Do any Eight experiments

1. To study truth tables of AND, OR, NOR, NAND, NOT and XOR Gates.
2. To verify the truth tables of RS, of JK and T Flip Flops.
3. To fabricate and test the truth table of half/full adder.
4. To design and implement a Modulo-N Counter.
5. To design and implement a Universal shift register
6. Design and fabrication of synchronous counter
7. Design of fabrication of combinational circuits using Multiplexers
8. To convert 8 bit Digital data to Analog value using DAC.
9. To convert Analog value into 8 bit Digital data using ADC
10. To design and fabricate the given sequential Circuits using Flip-flops as Memory elements
SYLLABUS FOR
B.E. MBA integrated in Computer Science & Engineering (2010-11)

FOURTH SEMESTER

Paper Title: ANALYSIS & DESIGN OF ALGORITHMS

Paper Code: CSE411
Credit: 04

Max. Marks (Final Exam): 50
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45
L T P: 3 1 0

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions by selecting at least two from each section.

Objectives: The subject will give an insight into performance analysis, measurements and optimization of the various algorithm development techniques. After completing this subject students will be able to choose one algorithm technique for any kind of problem.

SECTION – A

Introduction:
Role of Algorithms in Computing; Growth of functions: Asymptotic Notation, Standard notation & common functions; Introduction to Recurrences: substitution method, recursion-tree method, master method; Randomizing Algorithms;

(12)

Divide and Conquer:
Performance analysis of Binary Search, Merge sort, Quick sort, Selection sort;

Greedy Algorithms:
Elements of Greedy strategy, Activity Selection Problem, Knapsack problem, Single source Shortcut paths problem, Minimum Spanning tree problem and analysis of these problems.

SECTION – B

Dynamic Programming:
Elements of dynamic programming, Assembly-line scheduling problem, Matrix-chain multiplication, Multistage Graph, All Pairs Shortest paths, Longest common subsequence, 0/1 Knap Sack.

(12)

Back Tracking:
General method, 8 queen's problem, Graph coloring, 0/1 Knap Sack Problem

NP-Completeness:
Polynomial Time, polynomial-time verification, NP-completeness & reducibility, NP-complete problems

Text Book:
1. Introduction to Algorithms : Thomas H. Cormen, Charles E. Leiserson, Ronald L. Rivest
2. Fundamentals of Computer Algorithms : Ellis Horowitz, Sartaj Sahni (Galgotia)

References:
Paper Title: ANALYSIS & DESIGN OF ALGORITHMS (PRACTICAL)

Paper Code: CSE461
Credits: 02
Max. Marks: 50
L T P: 0 0 3

Note: Practical based on theory paper to solve problems using following methods:
1. Divide & Conquer
2. Greedy Method
3. Dynamic Programming
4. Backtracking

Paper Title: DATABASE MANAGEMENT SYSTEMS

Paper Code: CSE412
Credits: 04
Max. Marks (Final Exam): 50
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45
L T P: 3 1 0

Note: Examiner will set eight questions covering four questions from each section. Candidates will be required to attempt five questions, selecting at least two from each section.

Objectives: This course offers a good understanding of database systems concepts and prepares the student to be in a position to use and design databases for different applications.

SECTION – A

Introduction to Database Systems:

Physical Data Organization:
File Organization and Indexing, Index Data Structures, Hashing, B-trees, Clustered Index, Sparse Index, Dense Index, Fixed length and Variable Length Records.

Data Models:
Relational Model, Network Model, Hierarchical Model, ER Model: Entities, Attributes and Entity Sets, Relationships and Relationship Sets, Constraints, Weak Entities, Class Hierarchies, Aggregation, Conceptual Database Design with the ER Model, Comparison of Models.

The Relational Model:
Introduction to the Relational Model, ER to Relational Model Conversion, Integrity Constraints over Relations, Enforcing Integrity Constraints, Relational Algebra, Relational Calculus, Querying Relational Data.

SECTION – B

Relational Query Languages:
SQL: Basic SQL Query, Creating Table and Views, SQL as DML, DDL and DCL, SQL Algebraic Operations, Nested Queries, Aggregate Operations, Cursors, Dynamic SQL, Integrity Constraints in SQL, Triggers and Active Database, Relational Completeness, Basic Query Optimization Strategies, Algebraic Manipulation and Equivalences.
Database Design:
Functional Dependencies, Reasoning about Functional Dependencies, Normal Forms, Schema Refinement, First, (05) Second and Third Normal Forms, BCNF, Multi-valued Dependency, Join Dependency, Fourth and Fifth Normal Forms, Domain Key Normal Forms, Decompositions.

Transaction Management:
ACID Properties, Serializability, Two-phase Commit Protocol, Concurrency Control, Lock Management, Lost (06) Update Problem, Inconsistent Read Problem, Read-Write Locks, Deadlocks Handling, 2PL protocol.

Database Protection:
Threats, Access Control Mechanisms, Discretionary Access Control, Grant and Revoke, Mandatory Access (05) Control, Bell LaPadula Model, Role Based Security, Firewalls, Encryption and Digital Signatures.

Text Book:

References:

Paper Title: DATABASE MANAGEMENT SYSTEMS (PRACTICAL)

Paper Code: CSE462
Credits: 02
Max. Marks: 50
L T P: 0 0 3
Note: This practical will enable students to retrieve data from relational databases using SQL. Students will also learn about triggers, cursors, stored procedures etc.

1. Introduction to SQL and installation of SQL Server / Oracle.
2. Data Types, Creating Tables, Retrieval of Rows using Select Statement, Conditional Retrieval of Rows, Alter and (05) Drop Statements.
3. Working with Null Values, Matching a Pattern from a Table, Ordering the Result of a Query, Aggregate Functions, Grouping the Result of a Query, Update and Delete Statements.
7. Stored Procedures and Exception Handling.
8. Triggers and Cursor Management in PL/SQL.

Paper Title: OBJECT ORIENTED PROGRAMMING

Paper Code: CSE 414
Credits: 04
Max. Marks (Final Exam): 50
Time: 3 Hours
Max. Marks (Sessional Exam): 50
Total Lectures: 45
Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

Objectives: To understand the basic concepts of object oriented programming languages and to learn the techniques of software development in C++.

SECTION – A
1. Principles of Object Oriented Programming 2
2. Tokens, Expressions and control structures, various data types, and data structures, Variable declaration, Operators and scope of operators. 4
3. Pointers, Functions, Classes and Objects: Prototyping, referencing the variables in functions, memory allocation for classes and objects, Array of objects, pointers to member functions. 8
4. Constructors and Destructors, Operator Overloading and type conversion. 4
5. Inheritance: Derived classes, types of inheritance, and various types of classes. 5

SECTION – B
6. Virtual functions and Polymorphism. 5
7. I/O operations on files: Classes for files, Operations on a file, file pointers. 8
8. Exception Handling and Generic programming with templates: Introduction to templates, overloading of template functions and Inheritance. 9

Text Book:

References:
2. Bala Guruswamy : Object oriented programming with C++, TATA McGraw Hill

Paper Title: OBJECT ORIENTED PROGRAMMING & C++ (PRACTICAL)

Paper Code: CSE464
Credits: 02
Max. Marks: 50
L T P 0 0 3

Note: Programs related to:
1. Functions, Classes and Objects
2. Constructors and Destructors
3. Operator Overloading and Type Conversion
4. Inheritance and Virtual Functions
5. Files
6. Exception Handling and Generic Programming

Paper Title: Management of Information Technology (Theory)

Paper Code: IBM 401
Credits: 04
Max. Marks (Final Exam): 50
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45
L T P 3 1 0

Note: Examiner shall set eight questions, four from Part-A and four from Part-B of the syllabus. Candidate
will be required to attempt any five questions selecting at least two questions from Part-A and two from Part-B.

**Part A**

**Information Technology (IT)**

IT and society, IT infrastructure in India vis-à-vis developed nations (Telecommunication, Internet reach, PC, Broadband, Mobile Phones), IT applications in Healthcare & Education, meaning of E-Readiness and E-participation index as defined by United Nations, areas where growth is expected in future.

**System Investigation & Analysis, Networking**

System Analysis & Design, Symbols used in modeling a business process, Networking concepts, Ethernet, IP addressing, Functioning of Routers, Bridges, hubs and switches in a network, Telecommunication (GSM, CDMA, Wireless and other new technologies)

**Internet & Intranet**

Functioning of Internet, Encryption & Digital signatures, Firewalls, Fraud on the Internet, Virus, Hacking & Denial of Service attacks, Intellectual Property Protection on the Internet, Intranet & security

**Part B**

**E-Commerce & E-Governance**

E-Commerce models, Intermediaries in E-Commerce, E-Governance in India, study of successful E-Governance models like E-Choupal, E-Payments (E-Cash, E-Wallets) and major players in the area, Online Shopping, Revenue models for Online Shopping Portals, Web Auctions like EBay, dealing with E-Waste.

**Knowledge Management & Business Intelligence**

Meaning of Knowledge Management, Designing a Knowledge Management System, Nature & Scope of Business Intelligence, Software for Business Intelligence, Data Warehousing and Data Mining techniques.

**Recommended Books:**

1. Data warehousing fundamentals – Paulraj Ponniah, John Wiley
2. E-Governance: A comprehensive framework, D.N.Gupta, New Century Publications
3. Business Data Communications & Networking, Jerry FitzGerald, Alan Dennis, John Wiley
5. Information Technology for Management: Improving Performance in the Digital Economy, Efraim Turban, Linda Volonino, John Wiley

**Paper Title: MICROPROCESSOR**

**Paper Code:** EC416  
**Credits:** 04  
**Max. Marks (Final Exam):** 50  
**Max. Marks (Sessional Exam):** 50  
**Total Lectures:** 45  
**L T P:** 3 1 0  
**Time:** 3 Hours

**Note:** Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

**Objectives:** Provide students with the opportunity to gain experience in microprocessor-based system design, assembly language programming, and I/O interfacing to microprocessors.

**SECTION – A**

**Microprocessor Architecture and Microcomputer Systems:**

Microprocessor Architecture Memory, Input and Output Devices, The 8085 MPU, Example of an 808S-Based Microcomputer, Memory Interfacing, The SDK-85 Memory System.

**Interfacing I/O Devices:**


**Programming the 8085:**

**Programming Techniques with Additional Instructions:**

**SECTION – B**

**Counters and Time Delays:**
Counters and Time Delays, Hexadecimal Counter, Modulo: Ten, Counter, Generating Pulse Waveforms, Debugging Counter and Time-Delay Programs.

**Stack and Subroutines:**
Stack, Subroutine, Conditional Call and Return Instructions.

**Interrupts:**
The 8085 Interrupt, 8085 Vectored interrupts.

**Interfacing Data Converters:**

**General Purpose Programmable Peripheral Devices:**
The 82S5A Programmable Peripheral Interface, Illustration: Interfacing Keyboard and Seven- Segment Display, Illustration: Bi- directional-Data Transfer between Two Microcomputers, The 8254 Programmable Interval Timer, The 8259 A Programmable Interrupt Controller, Direct Memory. Access (DMA) and the 8257 DMA Controller, serial communication, Programmable communications interface 8251.

**Text Book:**
1. Ramesh S. Gaonkar : Microprocessor Programming and Architecture, Applications with the 8085, third edition, Publisher (Justified)

**References:**

**Paper Title: MICROPROCESSOR (PRACTICAL)**

**Paper Code:** EC 466  
Credit: 02  
**Max. Marks:** 50  
**L T P** 0 0 2

1. Familiarization of 8085 kits.
2. Verification of arithmetic and logic operations using above kits.(At least 5 programs)
3. Development of interfacing circuits of various control applications based on 8085.
4. Application of assembly language using 8085 instructions set to develop various programs.
5. Applications of data movement instructions to develop relevant programs.
SYLLABUS FOR
B.E. MBA integrated in Computer Science & Engineering (2010-11)

FIFTH SEMESTER

Paper Title: OPERATING SYSTEM

<table>
<thead>
<tr>
<th>Paper Code: CSE501</th>
<th>Max. Marks (Final Exam): 100</th>
<th>Time: 3 Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>Max. Marks (Sessional Exam): 50</td>
<td>Total Lectures: 45</td>
</tr>
</tbody>
</table>

Note: - Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

Objectives: This course should provide the students with good understanding of Operating System including its architecture and all its components. Good conceptions on all the subjects like processes, inter-process communication, semaphore, message passing, classical IPC problems, scheduling, memory management, file systems, security and protection mechanism, I/O hardware and software, deadlocks, etc. should be provided.

SECTION – A

Introduction: What is an O.S., O.S. Functions; Different types of O.S.: batch, multi-programmed, time sharing, real time, distributed, parallel; General structure of operating system, O/S services, system calls. (6)

Process Management: Introduction to processes - Concept of processes, process scheduling, operations on processes; Inter Process Communication, Critical Sections, Mutual Exclusion with Busy Waiting, Sleep and Wakeup, Semaphores, Message passing; CPU scheduling- scheduling criteria, preemptive & non-preemptive scheduling, Scheduling Algorithms: FCFS, SJF, RR and priority. (10)

Memory Management: background, logical vs. physical address space, memory management without swapping; swapping; contiguous memory allocation, paging, segmentation, segmentation with paging; Virtual Memory, demand paging, performance, page replacement, page replacement algorithms (FIFO, Optimal ,LRU); Thrashing. (6)

SECTION – B

File Systems: Files - file concept, file structure, file types, access methods, File attributes, file operations; directory structure, allocation methods (contiguous, linked, indexed), free-space management (bit vector, linked list, grouping), Protection mechanisms. (6)

Secondary Storage : Disk Structure, Disk Scheduling ( FCFS, SSTF, SCAN, C-SCAN, LOOK), Disk Management (Disk Formatting, Boot Blocks, Bad Blocks), Swap Space Management (Swap Space use, Swap Space Location, Swap Space Management) (6)

Deadlocks: Introduction to deadlocks, Conditions for deadlock, Resource allocation graphs, Deadlock Detection and Recovery, Deadlock Avoidance, Deadlock Prevention (6)

Case Studies: Brief introduction of MS-DOS, Windows, UNIX and LINUX. (5)

Text Book:

References:
**Paper Title: OPERATING SYSTEM LAB (PRACTICAL )**

**Paper Code: CSE551**  
**MM (External): 75**  
**MM (Internal): 75**

**Note:** Practical Problems related to

1. Learning Basic Features and Operating Environment of UNIX and LINUX.
2. Introduction to Shell and Shell Commands.
3. Shell programming: creating a script, making a script executable, shell syntax (variables, conditions, control structures, functions, commands.
4. Process: starting new process, replacing a process image, duplicating a process image, waiting for a process.
5. Programming with semaphores.

---

**Paper Title: SOFTWARE ENGINEERING**

**Paper Code: CSE502**  
**Max. Marks (Final Exam): 100**  
**Time: 3 Hours**  
**Max. Marks (Sessional Exam): 50**  
**Total Lectures: 45**

**Note:** Examiner will set eight questions covering four questions from each section. Candidates will be required to attempt five questions, selecting at least two from each section.

**Objectives:** This course aims to give students a theoretical foundation in software engineering. Students will learn about the principles and methods of software engineering, including current and emerging software engineering practices and support tools.

**SECTION – A**

**Introduction:**

**Software Process Models:**

**Project Management Concepts:**

**Software Requirements Analysis and Specification Concepts:**

**Software Design and Coding Concepts:**

**SECTION – B**
Testing:

Software Quality Assurance:
Software Quality, Software Quality Factors, Quality Assurance and Standards, Quality Planning, Quality Control, ISO 9000 Certification for Software Industry, SEI Capability Maturity Model and Comparison between ISO & SEI CMM. Introduction to Six Sigma, SPICE. (05)

Technical Metrics for Software:

CASE (Computer Aided Software Engineering) and Introduction to UML:

Text Book:

References:

Paper Title: SOFTWARE ENGINEERING (PRACTICAL)

Paper Code: CSE552
Max. Marks: 75

Note: This practical will enable students manage software projects using MS-Project. Students will learn about preparing analysis and design models using UML modeling concepts through Rational Rose. Students will also be exposed to CASE tools.

1. Study the features of MS-Project.
2. Use MS-Project to draft project plan for a particular project case study.
3. Use MS-Project to generate various reports like Gantt chart, Network diagram, Resource usage sheet.
4. Use MS-Project to track the progress of a project.
5. Study the concepts of UML modeling.
6. Use Rational Rose to generate use case diagrams.
7. Use Rational Rose to generate sequence diagrams.
8. Use Rational Rose to generate class diagrams.
9. Use Rational Rose to generate collaboration diagrams.

10. Study the features of a particular CASE tool for requirements specification, analysis, design and cost estimation.

11. Apply each of the above tools to a particular case study.

**Paper Title: COMPUTER NETWORK**

**Paper Code:** CSE503

Max. Marks (Final Exam): 100

Max. Marks (Sessional Exam): 50

**Time:** 3 Hours

**Total Lectures:** 45

**Note:** Examiner will set eight questions covering four questions from each section. Candidates will be required to attempt five questions, selecting at least two from each section.

**Objectives:** This course aims to give students a theoretical foundation in software engineering. Students will learn about the principles and methods of software engineering, including current and emerging software engineering practices and support tools.

---

**SECTION – A**

**Introduction:**
Data Communication: Components, Data Flow;
Network Categories: LAN, MAN, WAN (Wireless / Wired);
Network Software: Concept of layers, protocols, interfaces and services;
Reference Model: OSI, TCP/IP and their comparison; (06)

**Physical Layer:**
Concept of Analog & Digital Signal; Bit rate, Bit Length; Transmission Impairments: Attenuation, Distortion, Noise;
Data rate limits: Nyquist formula, Shannon Formula;
Multiplexing: Frequency Division, Time Division, Wavelength Division;
Transmission media: Twisted pair, coaxial cable, fiber optics, wireless transmission (radio, microwave, infrared);
Circuit Switching & Packet Switching.. (08)

**Data Link Layer:**
Error correction & Detection; Flow & Error Control;
Sliding window protocols: Stop & Wait ARQ, Go back n ARQ, Selective repeat ARQ; Examples of DLL Protocols-HDLC, PPP;
Medium Access Sub layer: Channel Allocation; Random Access: ALOHA, CSMA protocols; Controlled Access:
Polling, Reservation, Token Passing;
Examples of IEEE 802.3, 802.11 standards;k (10)

---

**SECTION – B**

**Network Layer:**
Logical Addressing: IPv4 and IPv6; Packet Formats & their comparison: IPv4 and IPv6;
Routing algorithms: Distance vector, Link State Routing, Hierarchical Routing, Broadcast & Multicast Routing;
Congestion Control: Principles of Congestion Control, Congestion prevention policies, Leaky bucket & Token bucket algorithms (10)

**Transport Layer:**
Addressing, flow control & buffering, multiplexing & de-multiplexing, crash recovery;
Example transport protocols: TCP, SCTP and UDP; (08)

**Application Layer:**
Network Security; Domain Name System; Simple Network Management Protocol; Electronic Mail; (03)

**Text Book:**
1. Andrew S. Tanenbaum: “Computer Networks”, Pearson Education

References:

Paper Title: COMPUTER NETWORK (Practical)

Paper Code: CSE553

MM (External): 75
MM (Internal): 75

Note: Practical Problems related to:
1. To familiarize with the various basic tools (crimping, krone etc.) used in establishing a LAN.
2. To study various topologies for establishing computer networks.
3. To familiarize with switch, hub, connecters, cables (cabling standards) used in networks
4. To familiarize with routers & bridges
5. To use some basic commands like ping, trace-root, ipconfig for trouble shooting network related problems.
6. To use various utilities for logging in to remote computer and to transfer files from / to remote computer.
7. To develop a program to compute the Hamming Distance between any two code words.
8. To develop a program to compute checksum for an ‘m’ bit frame using a generator polynomial.
9. To develop a program for implementing / simulating the sliding window protocol
10. To develop a program for implementing / simulating a routing algorithm
11. To study various IEEE standards (802.3, 802.11, 802.16)

Paper Title: PRINCIPLES OF PROGRAMMING LANGUAGES

Paper Code: CSE504
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

Objectives: This course should provide the students with a fairly good concept of fundamental concepts and design issues of programming languages and become familiar with major programming paradigms. Understand similarities and differences between models and know when to use them and also learn programming techniques appropriate for each model.

SECTION – A

Introduction:
Study of principles and major concepts in various programming paradigms like imperative,
functional, object-oriented and logic programming. Introduction to various phases of compilers, Formal translation models: BNF Grammars.

**Imperative programming:**
Location, reference and expressions, assignment and control, data types, blocks, procedures and modules.
Object Oriented Programming: Classes and objects, abstraction and encapsulation, inheritance, Polymorphism, virtual functions and classes, abstract classes.

**Logic Programming:**
Unification, SLD-resolution, Backtracking, Cuts.
Concepts Of Concurrent Programming: Processes, synchronization primitives.

**SECTION – B**

**Functional Programming:**
Functions as first class objects, higher order functions, polymorphic datatypes, type checking and type inference

**Introduction to storage management:**
Static storage management, Heap storage management.

**Illustration of the above concepts using representative languages:** C++, Java, and Prolog etc.

**Text Book:**
1. Pratt & Zelkowrtz, Programming Languages : Design & Implementation, Pearson Education

**References:**

**Paper Title:** Marketing Management

**Paper Code:** IBM 501

Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

**Note:** Examiner shall set eight questions, four from Part-A and four from Part-B of the syllabus. Candidate will be required to attempt any five questions selecting at least two questions from Part-A and two from Part-B.

**Objectives:**
(i) To understand the nature, tasks and the environment under which marketing operates. (ii) To study the theory, principles and practical aspects of various marketing functions. (iii) To learn to take marketing decisions.

**Part A**

**Introduction to Marketing:** [5]
Definition; Scope and Importance of Marketing; Key Customer Markets; Concepts/Philosophies of Marketing; Holistic Marketing Concept; Marketing Tasks; Marketing Mix

**Marketing Environment:** [5]
Marketing Environment; New Marketing Realities; New Consumer Capabilities; Demographic Environment; Social-Cultural Environment; Natural Environment; Technological Environment and Political-Legal Environment; SWOT analysis.
Analyzing Markets:
Marketing Research Process; Sources of data collection; factors influencing consumer behavior; buying decision process; post-purchase behavior; Organizational Buying; Stages in the Buying Process.

Market Segmentation:
Levels of market segmentation; segmenting consumer markets; Niche Marketing; segmenting business markets; Michael Porter’s five forces model; Analyzing competitors; strategies for market leaders; Targeting and Positioning.

Part B
Product Decisions:
Product characteristics; classifications; differentiation; packaging and labeling; Product Life Cycle.

Pricing Strategies:
Understanding Pricing; Setting the Price; Initiating and Responding to Price Changes; Reactions to Competitor’s Price Changes.

Marketing Channels:
Marketing Channels; Role of Marketing Channels; Identifying Major Channel Alternatives; Types of Intermediaries; Channel-Management Decisions, Retailing, Wholesaling.

Marketing Communication:
The Role of Marketing Communications; Communications Mix-Advertising, Sales Promotion, Public Relations and Publicity, Events and Experiences, Direct and Interactive Marketing, Personal Selling.

References:
2. Ramaswamy, V.S. & Namakumari, S: Marketing management, planning, implementation and control.

Paper Title: Human Resource Management

Paper Code: IBM 502

Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions, four from Part-A and four from Part-B of the syllabus. Candidate will be required to attempt any five questions selecting at least two questions from Part-A and two from Part-B.

Objectives: The objective of the paper is to make student aware of the various functions and importance of the HR department in any organization. It is basically concerned with managing the human resources, whereby the underlying objective is to attract retain and motivate the human resources in any organization, which is the most challenging and daunting look for any organization today.

Part A
Introduction:
Meaning, scope, objectives and functions of HRM; Importance of Human Resource Management; HRM & HRD a comparative analysis;
Environment of HRM: [5]
Role of government, internal and external forces; Human Resource Management practices in India.

Definition, objectives, process and importance; Job analysis, description, specification & job evaluation; Recruitment, selection, placement and induction process;

Human Resource Development: [6]
Concept, Employee training & development; Career Planning & development; Promotions, demotions, transfers, separation, absenteeism & turnover;

Part B

Job Compensation: [6]
Wage & salary administration, incentive plans & fringe benefits.

Performance Management: [6]
Concept & process, performance appraisal, Potential appraisal;

Quality of work life (QWL): [6]
Meaning, techniques for improving QWL.

Industrial Relations: [6]
Concept and theories, trade unions; Health, Safety & Employee welfare measures; Employee grievances and discipline, participation & empowerment; Introduction to collective bargaining.

References:
3. Dale Yoder: Personnel Management & Industrial Relation
SYLLABUS FOR
B.E. MBA integrated in Computer Science & Engineering (2010-11)

SIXTH SEMESTER

Paper Title: WEB TECHNOLOGIES

Paper Code: CSE601
L T P : 3 1 0
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

Objective: Aim of this paper is to familiarize the students with current technologies used in Web development and maintenance.

SECTION – A

INTERNET AND WORLD WIDE WEB:
Introduction, Internet Addressing, ISP, types of Internet Connections, Introduction to WWW, WEB Browsers, WEB Servers, URLs, http, WEB applications, Tools for WEB site creation.

HTML:
Introduction to HTML, Lists, adding graphics to HTML page, creating tables, linking documents, frames, DHTML and Style sheets.

Java Script:
Introduction, programming constructs: variables, operators and expressions, conditional checking, functions and dialog boxes, JavaScript DOM, creating forms, introduction to Cookies

SECTION – B

JAVA:
Introduction to java objects and classes, control statements, arrays, inheritance, polymorphism, Exception handling.

XML:
Why XML, XML syntax rules, XML elements, XML attributes, XML DTD displaying XML with CSS.

AJAX
Introduction, HTTP request, XMLHttpRequest, AJAX Server Script, AJAX Database.

PHP
Introduction, syntax, statements, operators, sessions, E-mail, PHP and MySQL, PHP and AJAX.

Text Books:

References:
1. Ivan Bayross : Web Enabled Commercial Application Development, BPB
2. Schafer : HTML,CSS, JavaScript,Perl, Python and PHP, Wiley India Textbooks.
Paper Title: WEB TECHNOLOGIES (Practical)

Paper Code: CSE651
Max. Marks(Final): 75
Max.Marks(Sessional): 75

Note: Students have to perform following experiments related to

1. Creation of Web pages using: HTML, DHTML
2. Creation of Web pages using JavaScript
3. Implementing basic concepts of Java
4. Creation of Web pages using AJAX
5. Database and AJAX
6. XML
7. PHP

Paper Title: Managerial Economics

Paper Code: IBM 601
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions, four from Part-A and four from Part-B of the syllabus. Candidate will be required to attempt any five questions selecting at least two questions from Part-A and two from Part-B.

Objectives: To provide students with an understanding of basic economic principles of production & exchange-essential tools in making business decisions in today’s global economy. The object presents the foundation to understanding how the economy works, covering microeconomic description of business applications, including pricing for profit maximization, price elasticity, market structures and modeling of business in varying economic climates. The focus is on market economics, the organization that operation there and their business strategies.

Part A
Introduction to Managerial Economics: [4]
Nature Scope and Importance of Managerial Economics, opportunity costs, incremental principle, time perspective, discounts and equi marginal principles.

Demand Concepts and Analysis: [4]
Individual Demand, Market Demand, Kinds of Demand, Determinants of Demand, Demand Functions, Functions, Demand Schedule and Law of Demand.

Theory of Consumer Behavior: [4]
Cardinal Utility Approach and Ordinal Utility (Indifference Curves) Approach;

Elasticity of Demand: [4]
Concept, Types, Measurement and importance.

Demand Forecasting: [5]
Sources of Data-Expert Opinions, Surveys and Market Experiments; Time Series Analysis-Trend Projection; Barometric Forecasting-Leading Indicators, Composite and diffusion Indices.
Part B

Production Function: [4]
Concept and types, Returns to Factor and Returns to Scale, Law of Variable Proportions.

Cost Concepts and Analysis: [4]
Concept of Cost, Short run and Long-run Cost Curves, Relationships among various costs, Break-even Analysis.

Revenue Curves: [4]
Concept and Types.

Perfect Competition: [4]

Monopoly: [4]
Characteristics, Equilibrium Price, Profit Maximizing output in Short Run and Long Run; Price Discrimination.

Imperfect Competition: [4]
Monopolistic Competition, oligopoly and Barriers to Entry.

References:
1. Craig Peterson, Lewis and Jain: Managerial Economics, Pearson Education
2. Mark Hirshey: Managerial Economics, Thomson
3. Dr. V. Panduranga Rao: Microeconomics-IBS Publication
7. K.K. Dewett: Modern Economic Theory, S. Chand & Sons, New Delhi
8. Mote, Paul Gupta: Managerial Economics, Vikas Publisher, New Delhi
9. A. Koutsoyiannis: Modern Microeconomics, Mc Millan, New Delhi
10. R.L. Varshney & K. L. Maheshwari: Managerial Economics, S.Chand & Sons, New Delhi

Paper Title: COMPUTER GRAPHICS

Paper Code: CSE603
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner will set eight questions covering four questions from each section. Candidates will be required to attempt five questions, selecting at least two from each section.

Objectives: This course offers a good understanding of computer graphics concepts and prepares the student to be in a position to understand and draw graphics for different applications.

SECTION – A

Overview of Graphics Systems:
Video Display Devices, Direct View Storage Tubes, Flat Panel Displays: Emissive and Non-Emissive Displays; Plasma Panel, Thin Film Electroluminescent and Liquid Crystal Displays, Color Display Techniques: Shadow Mask and Beam-penetration Methods, Three Dimensional Viewing Devices, Raster Scan Systems, Display Processor, Random Scan Systems, Co-ordinate Representations, Screen Coordinates.
Output Primitives:
Points and Lines, Line Drawing Algorithms: DDA Algorithm, Bresenham’s Line Algorithm, (08)
Parallel Line Algorithms, Circle Generating Algorithms, Ellipse Generating Algorithms, Pixel
Addressing and Object Geometry, Boundary Fill Algorithms, Flood Fill Algorithms, Character
Generation, Line, Area-Fill and Character Attributes.

Two Dimensional Geometric Transformations and Viewing:
Basic Transformations: Translation, Rotation and Scaling, Matrix Representations, Composite (08)
Transformations, Viewing Pipeline, Window to Viewport Coordinate Transformation, Clipping
Operations: Line, Polygon, Curve and Text Clipping.

SECTION – B
Three Dimensional Concepts, Transformations and Viewing:
Three Dimensional Display Methods, Three Dimensional Transformations; Three Dimensional (08)
Viewing Pipeline; Viewing Coordinates; Specifying the View Plane, Projections: Parallel
Projections, Perspective Projections.

Splines and Curves:
Curved Lines and Surfaces, Spline Representations, Cubic Splines, Bezier Curves and their (06)
properties, B-Spline Curves.

Visible Surface Detection Methods:
Classification of Visible Surface Detection Methods, Back Face Detection, Depth Buffer, A-Buffer, (07)
Scan Line and Depth-Sorting Methods, Wireframe Methods, Concepts of Computer Animation,
Design of Animation Sequences.

Text Book:
   Pearson Education.

References:
   Outlines, Tata McGraw-Hill.
3. N. Krishnamurthy : Introduction to Computer Graphics, Tata
   McGraw-Hill.
4. David F. Rogers, James Alan Adams : Mathematical Elements for Computer Graphics,
   Tata McGraw-Hill.
5. S. Harrington : Computer Graphics: A Programming Approach,
   Tata McGraw-Hill.

Paper Title: COMPUTER GRAPHICS (Practical)

Paper Code: CSE653 Max. Marks: 75

Note: This practical will enable students to draw basic graphics objects, perform transformations and build
graphics applications in C.

1. Introduction to graphics programming in C/C++.
2. Initializing graphics system. Basic graphics functions.
3. Drawing lines, circles, ellipses and other common objects.
4. Boundary Fill, Flood Fill and other region filling algorithms.
5. Two dimensional transformations (Translation, Rotation, Scaling Reflection, Shear) on different objects.

6. Clipping algorithms.

7. Programs related to splines and curves, animation sequences.

**Paper Title: ARTIFICIAL INTELLIGENCE**

**Paper Code:** CSE604

**Max. Marks (Final Exam):** 100

**Max. Marks (Sessional Exam):** 50

**Time:** 3 Hours

**Total Lectures:** 45

**Objectives:** To introduce the AI techniques to solve problems and search strategies to find optimal solution paths from start to goal state. The course also introduces different knowledge representation methods with introduction to natural language processing and expert systems

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

**SECTION – A**

**Introduction:**
Artificial Intelligence and its applications, Artificial Intelligence Techniques, criteria of success, Intelligent Agents, Nature and structure of Agents, Learning Agents

**Problem solving techniques:**
State space search, control strategies, heuristic search, problem characteristics, production system characteristics, Generate and test, Hill climbing, best first search, A* search, Constraint satisfaction problem, Mean-end analysis, Min-Max Search, Alpha-Beta Pruning, Additional refinements, Iterative Deepening

**Knowledge representation:**
Mapping between facts and representations, Approaches to knowledge representation, procedural vs declarative knowledge, Forward vs. Backward reasoning, Matching, conflict resolution, Non-monotonic reasoning, Default reasoning, statistical reasoning, fuzzy logic Weak and Strong filler structures, semantic nets, frame, conceptual dependency, scripts.

**SECTION – B**

**Planning:**
The Planning problem, planning with state space search, partial order planning, planning graphs, planning with propositional logic, Analysis of planning approaches, Hierarchical planning, conditional planning, Continuous and Multi Agent planning

**Learning :**
Forms of Learning, inductive learning, Decision trees, Computational learning theory, Logical formulation, knowledge in learning, Explanation based and relevance based learning, statistical learning, Learning with complete data and hidden variables, instance based learning, Neural Networks

**Introduction to Natural Language processing and Expert system:**

**Text Book:**
Paper Title: ARTIFICIAL INTELLIGENCE (Practical)

Paper Code: 654

Note: Practical Problems related to

1. Program Related to Problem Solving techniques of AI
   - Breadth First Search
   - Depth First Search
   - Heuristic Search
   - Best Search
   - Min-Max Search with alpha-beta pruning
   - Tic-Tac-Toe problem
   - N-Queens and N-Knight problem
   - Unification Algorithm

2. Introduction To AI Languages such as LISP, PROLOG

3. Representing Knowledge using RuleML

4. Using semantic Web

5. Knowledge of using Neural Networks, Fuzz logic, genetic algorithms

6. Other new AI Techniques

Paper Title: MODELING AND SIMULATION

Paper Code: CSE605

Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

Objectives: This course should provide the students with good understanding of various techniques of Simulation.

SECTION – A

Introduction: What is modeling and simulation, application areas, definition and types of system, model and simulation, introduction to discrete-event and continuous simulation.

Simulation Methods: Discrete-event Simulation, Time advance Mechanisms, Components and
organization of Discrete-event simulation, Flowchart of next-event time advance approach, (10)
Continuous Simulation.

Queueing Models: Single server queueing system, introduction to arrival and departure time,
flowcharts for arrival and departure routine. Event graphs of queueing model. Determining the (10)
events and variables, Event graphs for inventory model.

SECTION – B

Random Numbers: Introduction to Random Numbers, Importance of Random Numbers in (5)
Simulation, Mid-Square random number generator, Residue method, Arithmetic Congruential
generator, Testing Numbers for Randomness, Chi-Square Test.

Distribution Functions : Stochastic activities, Discrete probability functions, Cumulative (10)
distribution function, Continuous probability functions. Generation of random numbers following
binomial distribution, poisson distribution, continuous distribution, normal distribution, exponential
distribution, uniform distribution.

Simulation Languages: Basic Introduction to Special Simulation Languages:-GPSS/ MATLAB/ (5)
Network Simulators.

Text Books:
Hill Publication.
learning inc.

References:
India.
2. Rudra Pratap : “Getting Started with MATLAB 7”, Oxford
University Press.

Paper Title: MODELING AND SIMULATION (Practical )

Paper Code: CSE655

Note: Practical Problems related to

1. Programming in MATLAB: Introduction, Branching statements, loops, functions, additional data
types, plots, arrays, inputs/outputs etc.
2. Introduction regarding usage of any Network Simulator.

Paper Title: Corporate Legal Environment

Paper Code: IBM 602

Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50

Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions, four from Part-A and four from Part-B of the syllabus. Candidate
will be required to attempt any five questions selecting at least two questions from Part-A and two from
Part-B.

Objective: Corporate legal environment represents that external environment in which the organization has
to work. The course covers the basic laws which a student must be aware of.

**Part A**

**Information Technology Act-2000**

Objective of the act, documents excluded from the scope of the act, digital signatures, types of digital signatures in India, certifying authorities in India, regulation of certifying authorities, duties of subscribers, offences, appellate tribunal, penalties and adjudication

**Company Law**

Definition and nature of a company, kinds of companies, formation of a company, memorandum of association, articles of association, prospectus, membership in a company, shares, transfer and transmission of shares, meetings and proceedings.

**Part B**

**Patents Law**


**Consumer Protection Act 1986**

Definitions under the act: complaint, consumer, defect, deficiency, unfair trade practice, consumer protection councils, redressal machinery under the act, district forum, state commission, national commission

**References:**

1. Manish Arora, Guide to Patents Law, Universal Law Publishing Co
SYLLABUS FOR
B.E. MBA integrated in Computer Science & Engineering (2010-11)

SEVENTH SEMESTER

Paper Title: COMPILER DESIGN

<table>
<thead>
<tr>
<th>Paper Code: CSE701</th>
<th>Max. Marks (Final Exam): 100</th>
<th>Time: 3 Hours</th>
</tr>
</thead>
<tbody>
<tr>
<td>L T P: 3 1 0</td>
<td>Max. Marks (Sessional Exam): 50</td>
<td>Total Lectures: 45</td>
</tr>
</tbody>
</table>

**Note:** Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

**Objectives:** This course will provide the in-depth knowledge of different concepts involved while designing a compiler.

**SECTION – A**

**Introduction:** Compilers and Translators; The phases of the compiler – Lexical Analysis, Syntax Analysis, Intermediate Code Generation, Optimization, Code generation, Bookkeeping, Error handling. (5)

**Lexical Analysis:** The role of the lexical analyzer, Tokens, Patterns, Lexemes, Input buffering. Specifications of a token, Recognition of a tokens, Finite automata: Regular expressions, NFA, DFA. Design of a lexical analyzer generator. (5)

**Syntax Analysis:** The role of a parser, Context free grammars, Writing a grammar, Top down Parsing: Recursive decent parser, Predictive parser, Bottom up Parsing: Handles, Viable prefixes, Operator precedence parsing, LR parsers: SLR, LALR, CLR. Parser generator (YACC). Error Recovery techniques for different parsers. (12)

**SECTION – B**

**Syntax directed translation:** Syntax directed definitions, Synthesized and inherited attributes, Construction of syntax trees. (4)

**Run time environments:** Source language issues (Activation trees, Control stack, scope of declaration, Binding of names), Storage organization (Subdivision of run-time memory, Activation records), Storage allocation strategies, Symbol tables: storage, data structures used. (6)

**Intermediate code generation:** Intermediate languages, Graphical representation, Three-address code, Implementation of three address statements (Quadruples, Triples, Indirect triples). (3)

**Code optimization and code generation:** Introduction, Basic blocks & flow graphs, DAG, principle sources of optimization: loop optimization, eliminating induction variable, eliminating common sub-expression, loop unrolling, loop jamming etc. Peephole optimization, Issues in the design of code generator, a simple code generator, Register allocation & assignment. (10)

**Text Book:**


**References:**

1. Dhamdhere : Compiler Construction- Principles and Practice Macmillan, India 198
2. Holub : Compiler Design in C. PHI.

Paper Title: COMPILER DESIGN (PRACTICAL)
Note: Students have to perform the below-mentioned experiments using any language or tool available.

1. Implementation of lexical analyzer for a hypothetical language.
2. Implementation of LL parser.
3. Implementation of SLR parser.
4. Implementation of CLR parser.
5. Implementation of LALR parser.

**Paper Title: MULTIMEDIA SYSTEM DESIGN**

Objectives: This Course introduces the multimedia systems and their applications to students. This course covers the different compression standards used in multimedia, some current technology and related issues.

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

**SECTION – A**

**Introduction:**
Multimedia and its types, Introduction to Hypermedia, Hyper Text, Multimedia Systems and their Characteristics, Challenges, Desirable Features, Components and Applications, Trends in Multimedia

**Multimedia Technology:**

**Storage Media:**
Magnetic and Optical Media, RAID and its levels, Compact Disc and its standards, DVD and its standards, Multimedia Servers

**Audio:**

**SECTION – B**

**Image, Graphics and Video:**

**Video and Audio Compression:**
Classifying Compression Algorithms, Lossless Compression Algorithms, Entropy Encoding, Run-length Encoding, Pattern Substitution, Basics of Information theory, Huffman Coding, Huffman Coding of Images, Adaptive Huffman Coding, Arithmetic Coding, Lempel-Ziv-Welch (LZW) Algorithm, Source Coding Techniques, Transform Coding, Frequency Domain Methods,
Multimedia Communication:
Building Communication network, Application Subsystem, Transport Subsystem, QOS, Resource Management, Distributed Multimedia Systems

Text Book:
1. Ralf Steinmetz and Klara Nahrstedt: Multimedia Computing Communications and Applications By Pearson Educations

References:

Paper Title: SOFTWARE TESTING AND QUALITY ASSURANCE

Paper Code: CSE703
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner will set eight questions covering four questions from each section. Candidates will be required to attempt five questions, selecting at least two from each section.

Objectives: This course offers a good understanding of the concepts, methods and techniques of software testing and quality assurance and prepares students to be in a position to develop error free and quality software.

SECTION – A
Introduction:

Software Quality Assurance Concepts and Standards:

Risk Management and Change Management:

SECTION – B
Software Testing:
Testing Techniques:

Testing Process:

Text Book:

References:

Paper Title: SOFTWARE TESTING AND QUALITY ASSURANCE (Practical)

Paper Code: CSE753 Max. Marks: 75

Note: This practical will enable students to use and design software quality assurance and testing tools.

1. Study of different quality assurance and software testing tools.
2. Write programs to perform different types of testing.
3. Use of a software testing tool.
4. Use of a quality assurance tool.
5. Design and Implementation of a quality assurance / software testing tool.
Paper Title: Entrepreneurship and Project Management

Paper Code: IBM 701
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions, four from Part – A and four from Part – B of the syllabus. Candidate will be required to attempt any five questions selecting at least two from each part.

Objectives: To understand the concept and importance of entrepreneurship and its role in business life for individual, corporate growth and development..

Part – A

Introduction: Definition, Need, Classification, Process, Scope and Nature of Entrepreneurship, Difference between entrepreneur, entrepreneurship, enterprise and entrepreneurial. (6 hrs)

Concepts of Entrepreneurship: Factors affecting entrepreneurship, Functions of entrepreneurship, Culture of modern entrepreneurship, Competencies and traits of entrepreneurship, Entrepreneurial ventures in India. (6 hrs)

Types of entrepreneurship: Role of mitigation in entrepreneurship, Myths of entrepreneurship, Role of family structure in entrepreneurship. Women entrepreneurs, Internet Entrepreneurship, Case Study. (8 hrs)

Part – B

Project Management: Meaning, Concept, Characteristics, Types, Importance, and Steps in Project Identification, Concept of Project Management, Forms of Project Organization. (11 hrs)

Project system: Life cycle of system; Project Manager: Attributes; (7 hrs)


Text Books:

Reference Books:
4. Hisrich, Robert D. “Entrepreneurship”.
Paper Title: Industrial Relations

Objective: The objective of this course is to make the students familiar with various industrial relations approaches and procedure to resolve industrial dispute and with various industrial relations legislations

Part A

**Industrial Relations** - Concepts & Definitions - Impact of Industrial Revolution on Industrial Relations - Indian Industrial Workers - Work Culture - wages and industrial relations - Objectives of Industrial Relations. 6hrs

**Four Actors in Industrial Relations** – The Workers – management – Government – Society & their Importance in maintaining Industrial Relations. 4hrs


**Industrial dispute** – Meaning – Various causes of disputes – Management causes – Trade Union causes – Work related causes – Organisational causes – Government causes – Disputes arising out of terms and conditions. 6hrs

**Strikes** – Types of strikes 2hrs

Part B

**Trade Union** – Definition structure – features – Functions of trade Union – Trade Union Security – Types of Trade Unions – Reformist trade union 4hrs

**Collective Bargaining** – Meaning – Importance – Issues – Features and Process. 2hrs

**Industrial Democracy and Workers’ participation in Management** -Role of workers and management in the industry. 3hrs

**Employees’ grievance** – Meaning – Causes – Grievance redressal procedures. 2hrs

**Industrial Relations Legislations:**

a) Trade Union Act,1926
b) Industrial Disputed Act, 1947
   (Tripartite and Bipartite bodies : Resolution of Industrial Disputes by conciliation , Arbitration, Ad-judication, Prohibition of strikes – Lockout – Layoff – Retrenchment – Closure etc.)
c) Industrial Employment (Standing Orders) Act, 1946
   (Terms and Conditions of employment and disciplinary procedure) 6hrs

**Other Legislations**

Payments of Wages Act,1936
Minimum Wages Act,1948
The Factories Act,1948 6hrs

**Suggested Readings:**

1. Industrial Law –P.L.Malik,Eastern Book Co.Lucknow
SYLLABUS FOR
B.E. MBA integrated in Computer Science & Engineering (2010-11)
EIGHTH SEMESTER

Paper Title: ADVANCED DATABASE SYSTEMS

Paper Code: CSE801
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Total Lectures: 45
Time: 3 Hours

Note: Examiner will set eight questions covering four questions from each section. Candidates will be required to attempt five questions, selecting at least two from each section.

Objectives: This course offers a good understanding of advanced database concepts and technologies. It prepares the student to be in a position to use and design databases for a variety of applications.

SECTION – A

Introduction to Database Systems:
Database System Concepts and Architecture, Data Models, Data Independence, SQL: DDL, DML, DCL, Normalization: 1NF, 2NF, 3NF, BCNF, 4NF, 5NF. (06)

Query Processing and Optimization:
Query Processing, Syntax Analyzer, Query Decomposition, Query Optimization, Heuristic Query Optimization, Cost Estimation, Cost Functions for Select, Join, Query Evaluation Plans. (06)

Transaction Processing and Concurrency Control:
Transaction Processing Concepts, Concurrency Control Techniques: Two-phase Locking, Timestamp Ordering, Multiversion, Validation, Multiple Granularity Locking. (05)

Object Oriented and Object Relational Databases:
Object Oriented Concepts, Object Oriented Data Model, Object Definition Language, Object Query Language, Object Relational Systems, SQL3, ORDBMS Design. (05)

SECTION – B

Distributed Databases:
Distributed Database Concepts, Advantages and Disadvantages, Types of Distributed Database Systems, Data Fragmentation, Replication and Allocation Techniques for Distributed Database Design, Five Level Schema Architecture, Query Processing, Concurrency Control and Recovery in Distributed Databases. (06)

Backup and Recovery:
Types of Database Failures, Types of Database Recovery, Recovery Techniques: Deferred Update, Immediate Update, Shadow Paging, Checkpoints, Buffer Management. (05)

Introduction to Data Warehousing and Data Mining:
Introduction to OLAP, OLTP, Data Warehouse, Data Marts, Data Mining, Data Mining Process. (05)

Commercial Databases:
Commercial Database Products, Familiarity with IBM DB2 Universal Database, Oracle, Microsoft SQL Server, MySQL, their features. (07)

Text Book:
Paper Title: DIGITAL IMAGE PROCESSING & COMPUTER VISION

Paper Code: CSE802

Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Total Lectures: 45

Time: 3 Hours

Note: Examiner shall set eight questions covering four questions from each section. Candidate will be required to attempt five questions, at least two from each section.

Objectives: To introduce the various image processing techniques and their applications in different domains. To get students acquainted with computer vision.

SECTION – A
Introduction to Image Processing:
Digital Image representation, Sampling & Quantization, Steps in image Processing, Image acquisition, color image representation, color models

Image Transformation & spatial Filtering:
Intensity transform functions, histogram processing, Spatial filtering, fourier transforms and its properties, Walsh transform, Hotelling transforms, Haar and slant transforms, Hadamard transforms, frequency domain filters, Homomorphic Filtering, Pseudo coloring, color transforms

Image Restoration:
Image degradation and restoration process, Noise Models, Noise Filters, degradation function, Inverse Filtering

SECTION – B
Image Compression:
Coding redundancy, Interpixel redundancy, Psychovisual redundancy, Huffman Coding, Arithmetic coding, Lossy compression techniques, JPEG Compression

Image Segmentation & Representation:
Point, Line and Edge Detection, Thresholding, Edge and Boundary linking, Hough transforms, Region Based Segmentation, Boundary representation, Boundary Descriptors, Regional Descriptors

Computer Vision:
The role of Computer Vision, applications, Exemplars: Face Recognition, Medical Image Analysis, Recognizing man-made objects, representing knowledge of expected image contents. Matching models to image data

Text Book:
References:


Paper Title: DIGITAL IMAGE PROCESSING & COMPUTER VISION (Practical)

Paper Code: CSE852

Note: Students are required to complete all the practical by implementing them in any of the programming language such as Java, C/C++, C#, MATLAB

1. Reading and displaying images in different formats using different color models.
2. Converting color images into monochrome images.
3. Understanding brightness, contrast and intensity concept of images
4. Images enhancements using grey level transformations
5. Image enhancements using spatial filters
6. Image enhancements in frequency domain
7. Homomorphic Filtering
8. Image Noise removal and inverse filtering of images
9. Image color enhancements using pseudo coloring techniques
10. Point, Line, Edge and Boundary Detections in images
11. Histogram Matching and specification on images
12. Boundary Linking techniques on images
13. Thresholding of Images
14. Magnification of Images
15. Image representation and Description techniques
Paper Title: Research Methodology

Paper Code: IBM-801
Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions, four from Part – A and four from Part – B of the syllabus. Candidate will be required to attempt any five questions selecting at least two from each part.

Objectives: The main objective of this subject is to help the students to understand the nature, scope, complexities and process of defining a business research question. The learning focus is on developing business research skills to underpin the approach taken to a work integrated project.

PART-A

Introduction: Meaning, Features, Objectives/Motives, Steps & Types of Research; Attributes of good Research, Research Methods and Research Methodology; Research Process, Significance of Research in Managerial decision making. (8 hrs)

Research Design: Meaning, Characteristics and various concepts relating to research design and classification of research design, Importance, Design for different types of research. (5 hrs)

Concepts: Concept, Constructs, case study and Variables. (3 hrs)

Formulation of Hypothesis: Meaning, Characteristics and Concepts relating to testing of Hypothesis, Types of Hypothesis, Procedure of testing Hypothesis. Chi – Square test. (5 hrs)

PART-B

Data Collection: Sources of Data-Primary/Secondary Methods of collecting data; direct personal interview, indirect oral interview, information through local agencies, mailed questionnaire method, schedule sent through enumerators; questionnaire and its designing and characteristics of a good questionnaire. (8 hrs)

Sampling Design: Meaning, Need, Purpose and Principles of Sampling, Types of Sampling. (5 hrs)

Data Analysis & Interpretation: Introduction to Multivariate analysis- Multiple and partial correlation, multiple regression analysis (with two independent variables), specification of regression models and estimation of parameters, interpretation of results. Analysis of Variance (ANOVA)-One way and Two way ANOVA. (Numerical not to be asked) (6 hrs)

Report writing: Style/format, contents and essential steps for report writing. (5 hrs)

Text Books:

Reference Books:
2. Ranjit Kumar: Research Methodology, Pearson Education.
Paper Title: JAVA TECHNOLOGIES (PRACTICAL)

Paper Code: CSE855

Max. Marks (Final): 75
Max. Marks (Sessional): 75

Note: Students have to perform the following experiments

1. Implementation of classes, inheritance, overloading.
2. Implantation of packages and interfaces
3. Implantation of threads.
4. Implementation of Applets, mouse events, and keyboard events.
5. Connecting to Database using JDBC.
6. Deployment of Servlets, JSP and EJB.

Paper Title: Financial Management

Paper Code: IBM-802

Max. Marks (Final Exam): 100
Max. Marks (Sessional Exam): 50
Time: 3 Hours
Total Lectures: 45

Note: Examiner shall set eight questions, four from Part – A and four from Part – B of the syllabus. Candidate will be required to attempt any five questions selecting at least two from each part.

Objective: The objective of this course is to create basic understanding of corporate finance, Capital Budgeting decisions, working capital management, project management etc in the Engineering profession.

PART-A

Corporate Finance

Working Capital Management
Concept, Need and Types of Working Capital Management of Cash, Inventory, Accounts Receivable and Accounts Payable, Over and Under Trading. 6hrs

Ratio Analysis

Techniques of capital Budgeting: Various types of Capital Budgeting, Payback method, NPV, IRR, ARR, Capital Rationing. 6hrs
Cost of Capital: Cost of Enquiry, Cost of Debt, Cost of Retained Earnings, Weighted Average cost of Capital.

PART-B

Portfolio Management
Securities, Markets, Stock Exchanges, Risk Return, Relationship, Portfolio Structures.

Dividends, Bonus and Rights
Dividends Policy, Legal Requirements for issue of Bonus Shares, Right Shares and Share Premium.

Financial Management in Public Enterprises.
Concept & its applications

Concept of Leverages
Operating, Financial and Combined

Optimum Capital Structure
EBIT-EPS Relationship, Tax shield Analysis

Suggested Readings: